

IN THE CLAIMS

Please amend the claims as follows:

1. (Original) A method for storing data in a flash memory device having at least a code bank and a data bank, comprising:

writing data to the data bank under control of a flash driver in the code bank when sufficient space is expected to exist in the data bank; otherwise

writing data to the code bank under control of a flash driver in a storage device external to the flash memory device.

2. (Original) The method of Claim 1, wherein the flash memory device is accessed by a wireless communication device processor.

3. (Original) The method of Claim 2, wherein the storage device external to the flash memory device is a RAM accessed by the processor.

4. (Original) The method of Claim 3, wherein copies of the flash driver are in both the RAM and the code bank.

5. (Original) The method of Claim 1, further comprising preventing the flash driver from accessing code in the code bank when performing operations on the flash memory device.

6. (Original) A wireless communication device, comprising:

at least one processor;

at least one RAM communicating with the processor;

at least one read-while-write flash memory device communicating with the processor; and
at least one flash driver controlling operation of the flash memory device, the flash driver
being executable from the RAM.

7. (Original) The device of Claim 6, wherein the flash driver is prevented from
accessing code in a code bank of the flash memory device at least when performing operations on
the flash memory device.

8. (Original) The wireless communication device of Claim 6, wherein the flash
driver is executed by the processor.

9. (Original) The wireless communication device of Claim 6, wherein the flash
driver is executed to download at least one game into the wireless communication device.

10. (Original) The wireless communication device of Claim 6, wherein one and only
one copy of the flash driver exists in the wireless communication device, and that in the RAM.

11. (Currently Amended) A wireless communication device comprising:
at least one MSM processor;
at least one RAM accessed by the processor; and
at least one read-while-write flash memory accessed by the processor, the processor
writing data to the flash memory by accessing a flash driver instantiated in the RAM.

12. (Original) The wireless communication device of Claim 11, wherein the flash
memory includes at least a code bank and a data bank.

13. (Original) The wireless communication device of Claim 12, wherein the processor accesses a flash driver in the RAM to write program data to the code bank.

14. (Original) The wireless communication device of Claim 11, wherein the flash driver is executed to download at least one game into the wireless communication device.

15. (Original) The wireless communication device of Claim 11, wherein one and only one copy of the flash driver exists in the wireless communication device, and that in the RAM.

16. (Original) The device of Claim 12, wherein the flash driver is prevented from accessing code in the code bank at least when performing operations on the flash memory.

17. (Original) A system for storing data in a flash memory device having at least a code bank and a data bank, comprising:

means for writing data to the data bank under control of a flash driver in the code bank when sufficient space is expected to exist in the data bank; and

means for otherwise writing data to the code bank under control of a flash driver in a storage device external to the flash memory device.

18. (Original) The system of Claim 17, wherein the flash memory device is accessed by a wireless communication device processor.

19. (Original) The system of Claim 18, wherein the storage device external to the flash memory device is a RAM accessed by the processor.

20. (Original) The system of Claim 17, wherein copies of the flash driver are in both the RAM and the code bank.

21. (Original) The system of Claim 17, comprising means for preventing the flash driver from accessing code in the code bank when performing operations on the flash memory device.

22. (Original) A computer-readable medium embodying codes for implementing a method for storing data in a flash memory device having at least a code bank and a data bank, the method comprising:

writing data to the data bank under control of a flash driver in the code bank when sufficient space is expected to exist in the data bank; otherwise

writing data to the code bank under control of a flash driver in a storage device external to the flash memory device.

23. (Original) The medium of Claim 22, the method further comprising preventing the flash driver from accessing code in the code bank when performing operations on the flash memory device.

24. (New) The method of Claim 1, wherein writing data to the code bank further comprises preventing access to the flash driver in the code bank.

25. (New) The method of Claim 1, further comprising mapping of the flash driver to at least one of the code bank and the storage device at a compile time.

26. (New) The method of Claim 1, further comprising mapping of the flash driver to at least one of the code bank and the storage device at an execution time.

27. (New) A wireless communication device, comprising:

a processor;

a flash memory device comprising a code bank and a data bank, wherein the code bank comprises a local copy of a flash driver operable to be accessed by the processor to perform operations on the data bank; and

a storage device, external from the flash memory device, comprising an external copy of the flash driver operable to be accessed by the processor to store data in the code bank.

28. (New) The wireless communication device of Claim 27, wherein the processor is operable to access only the local copy of the flash driver to perform the operations on the data bank, and further wherein the processor is operable to access only the external copy of the flash driver to store the data on the code bank when the code bank has space to store the data.

29. (New) A method for storing data in a read-while-write flash memory device having a code bank and a data bank, comprising:

determining if space is expected to exist in the code bank in which to write application data;

mapping a flash driver to a storage device external from the flash memory device if the space is expected to exist in the code bank, and writing at least a portion of the application data to the code bank under control of the flash driver in the storage device; and

mapping a flash driver to the code bank on the flash memory device if the space is not expected to exist in the code bank, and writing the application data to the data bank under control of the flash driver in the code bank.

30. (New) The method of Claim 29, wherein the mapping of the flash driver to the storage device and the mapping of the flash driver to the code bank each further comprises mapping at compile time.

31. (New) The method of Claim 29, wherein the mapping of the flash driver to the storage device and the mapping of the flash driver to the code bank each further comprises mapping dynamically at execution time.

32. (New) The method of Claim 29, further comprising preventing access to the flash driver mapped to the code bank when writing the application data to the code bank.